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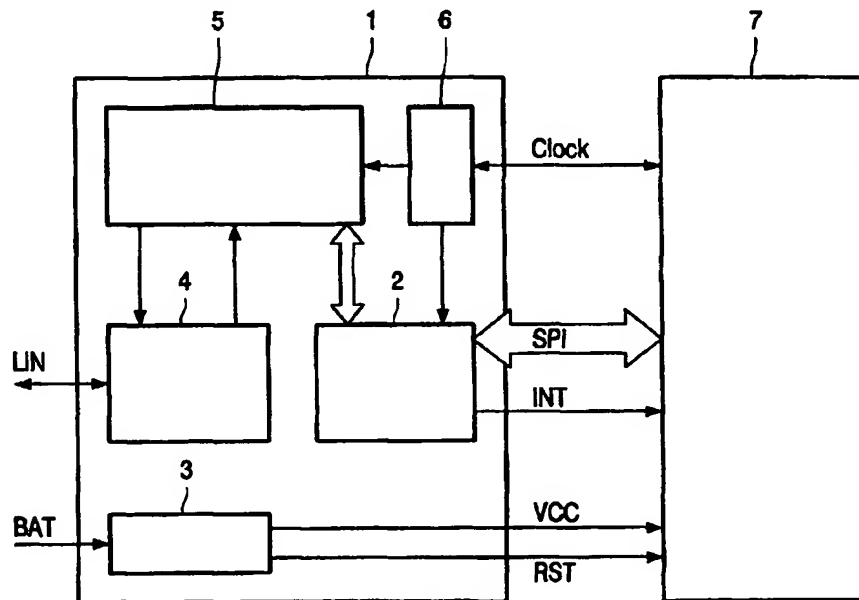
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(54) Title: INTEGRATED CIRCUIT



(57) **Abstract:** An integrated circuit having-a system base chip (1) that has basic functions for a transmitting and/or receiving system for a vehicle data bus, namely at least a system voltage supply (3), a system reset (3) and a monitoring function (2),-an interface circuit (4, 5) that, in a self-contained fashion, runs at least parts of a data bus protocol, and in particular the LIN (Local Interconnect Network) protocol, that performs detection of the bit-rate of received data, and that is capable of passing on at least one received or transmitted byte.-a serial/parallel converter (6) that makes use in its conversion of the bit-rate detected by the interface circuit (4, 5).

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